



JFW

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): André DeHon, et al.) Re: Information Disclosure
) Statement
Serial No.: 10/627,405) Group: 2817
)
Filed: July 24, 2003) Examiner: Crane, Sara W.
)
For: "STOCHASTIC ASSEMBLY OF) Our Ref: B-5025NP 620801-2
SUBLITHOGRAPHIC NANOSCALE)
INTERFACES") Date: February 7, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria VA, 22313-1450

Sir:

In accordance with the Applicants' duty to disclose information which may be material to the examination of this application, the undersigned respectfully requests that the Examiner consider on the merits the documents listed on the enclosed Form PTO-1449 (modified). We are enclosing herewith a copy of each document listed on the enclosed Form PTO-1449 (modified).

The documents listed on the enclosed Form PTO-1449 (modified) include those cited in the International Search Report the corresponding PCT Patent Application No. PCT/US2003/23198 which was mailed on November 24, 2004. A copy of the Search Report (5 pages) is enclosed herewith.

The documents listed on the enclosed Form PTO-1449 (modified) include those cited in the International Search Report and Written Opinion for a related PCT Patent Application No. PCT/US2004/017075 which was mailed on December 17, 2004. A copy of the Search Report and Written Opinion (14 pages) is enclosed herewith.

The undersigned certifies that each item of information contained in the this IDS was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS.

02/11/2005 HDENESS1 00000007 10627405

01 FC:1806

180.00 OP

Information Disclosure Statement
USSN 10/627,405
February 7, 2005
Page 2

This IDS is being submitted after receiving a Notice of Allowance, but prior to the payment of an issue fee. Thus, the references listed in this IDS should be fully considered, in accordance with 37 C.F.R. § 1.97(d). Enclosed is a check for \$180.00, which is the fee set forth in 37 C.F.R. § 1.17(p).

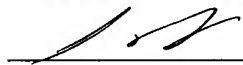
The filing of this IDS shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this Information Disclosure Statement shall not be construed as an admission against interest in any manner. (Notice of January 9, 1992, 1135 O.G. 13-25, at 25.)

Information Disclosure Statement
USSN 10/627,405
February 7, 2005
Page 3

The person making this statement is the practitioner who signs below on the basis of information supplied by an individual associated with the filing and prosecution of this application (37 C.F.R. § 1.56(c)) and on the basis of information in the practitioner's file.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450", on February 7, 2005 by Shana Morda.



Respectfully submitted,



Robert Popa
Attorney for Applicant
Reg. No. 43,010

LADAS & PARRY
5670 Wilshire Boulevard
Suite 2100
Los Angeles, CA 90036
(323) 934-2300

Enclosures: Form PTO-1449 (modified) (1 page)
Copy of International Search Report for PCT/US03/23198
(5 pages)
Copy of International Search Report and Written
Opinion for PCT/US2004/017075 (14 pages)
Copy of documents listed in Form PTO-1449 (modified)
Check for \$180.00



Information Disclosure Statement

SSN 10/627,405

February 7, 2005

Page 4

Form PTO-1449 (Modified)	ATTY DOCKET NO. B-5025NP 620801-2	U.S. SERIAL NO. 10/627,405
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANTS André DeHon, et al.	
	FILING DATE July 24, 2003	GROUP 2817

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	ISSUE DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DeHon, A., "Array-Based Architecture for FET-Based, Nanoscale Electronics," <i>IEEE Transactions on Nanotechnology</i> , Vol. 2, No. 1, pp. 23-32 (March 2003).

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.